

Fig. 1

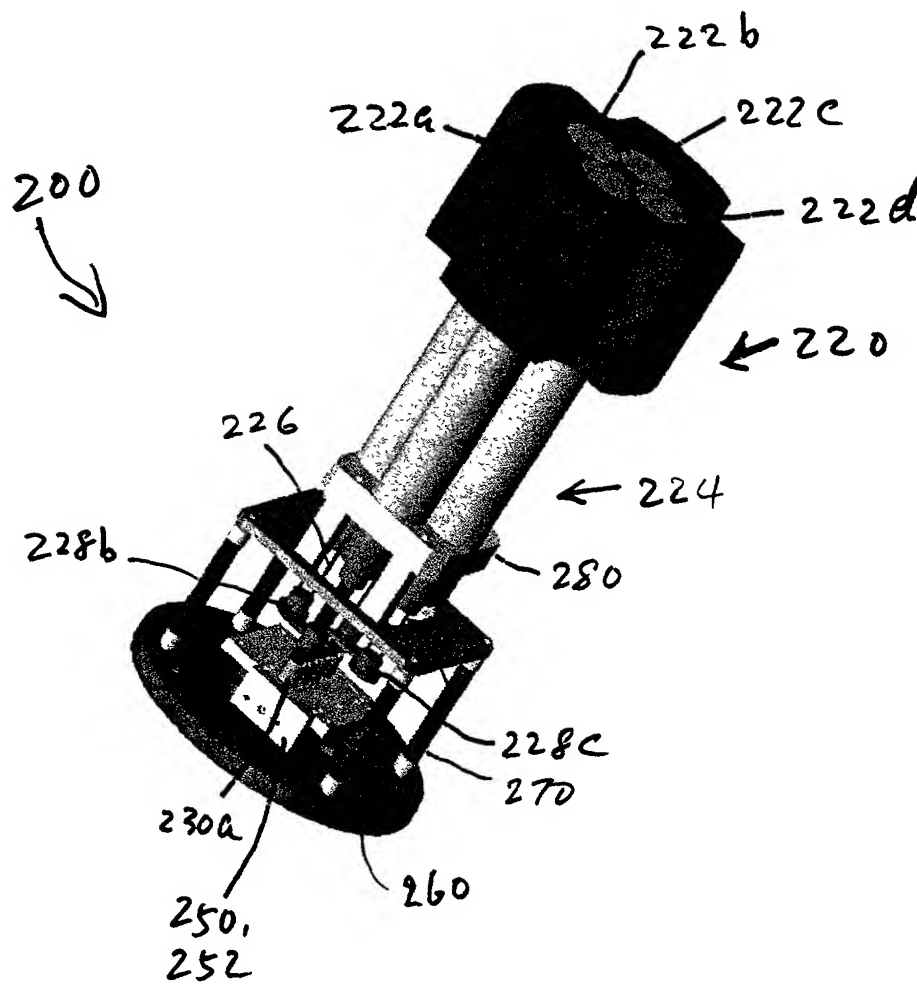


Fig. 2

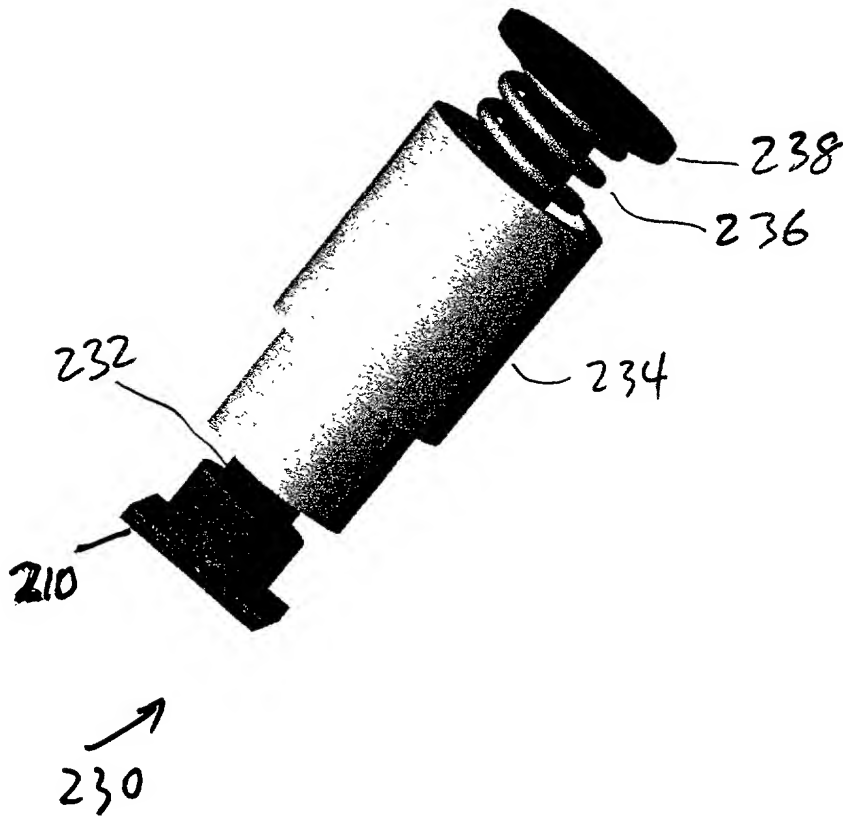


Fig. 3

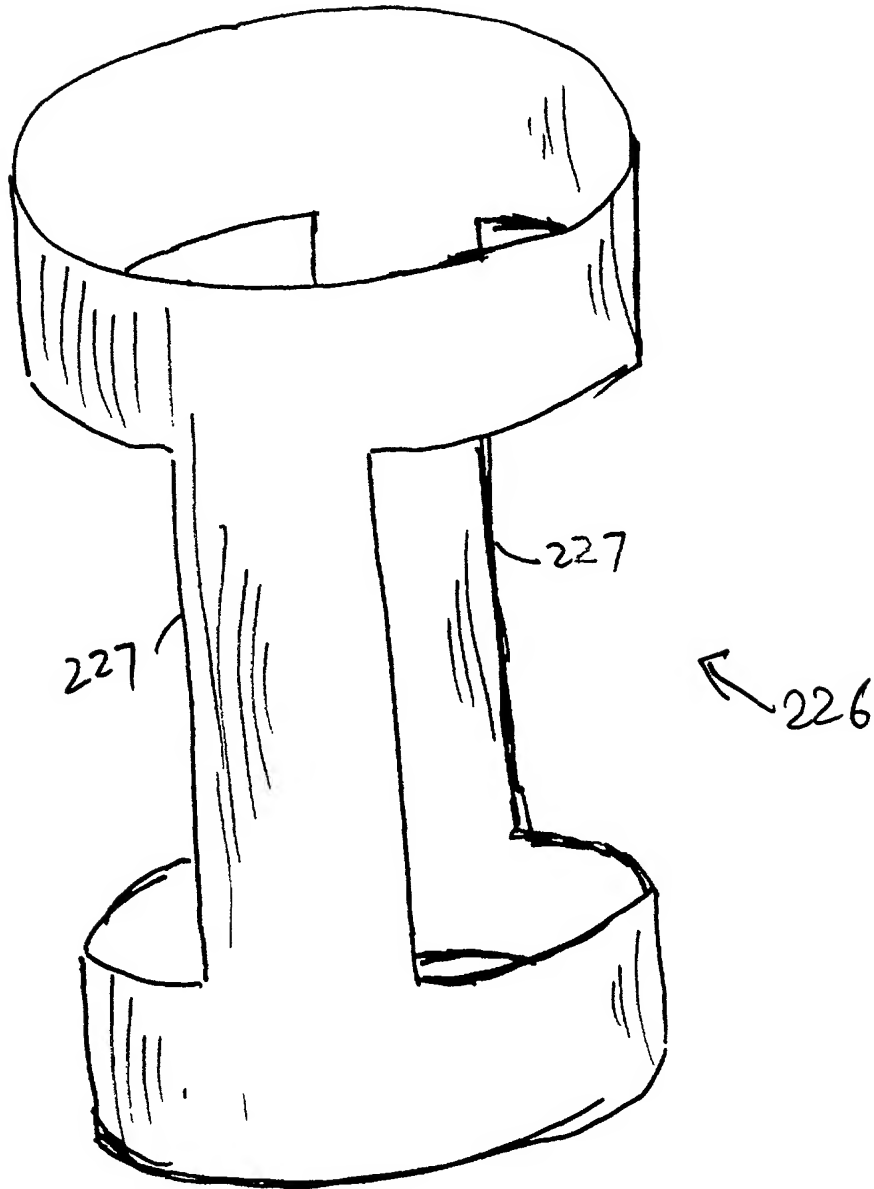


Fig. 4

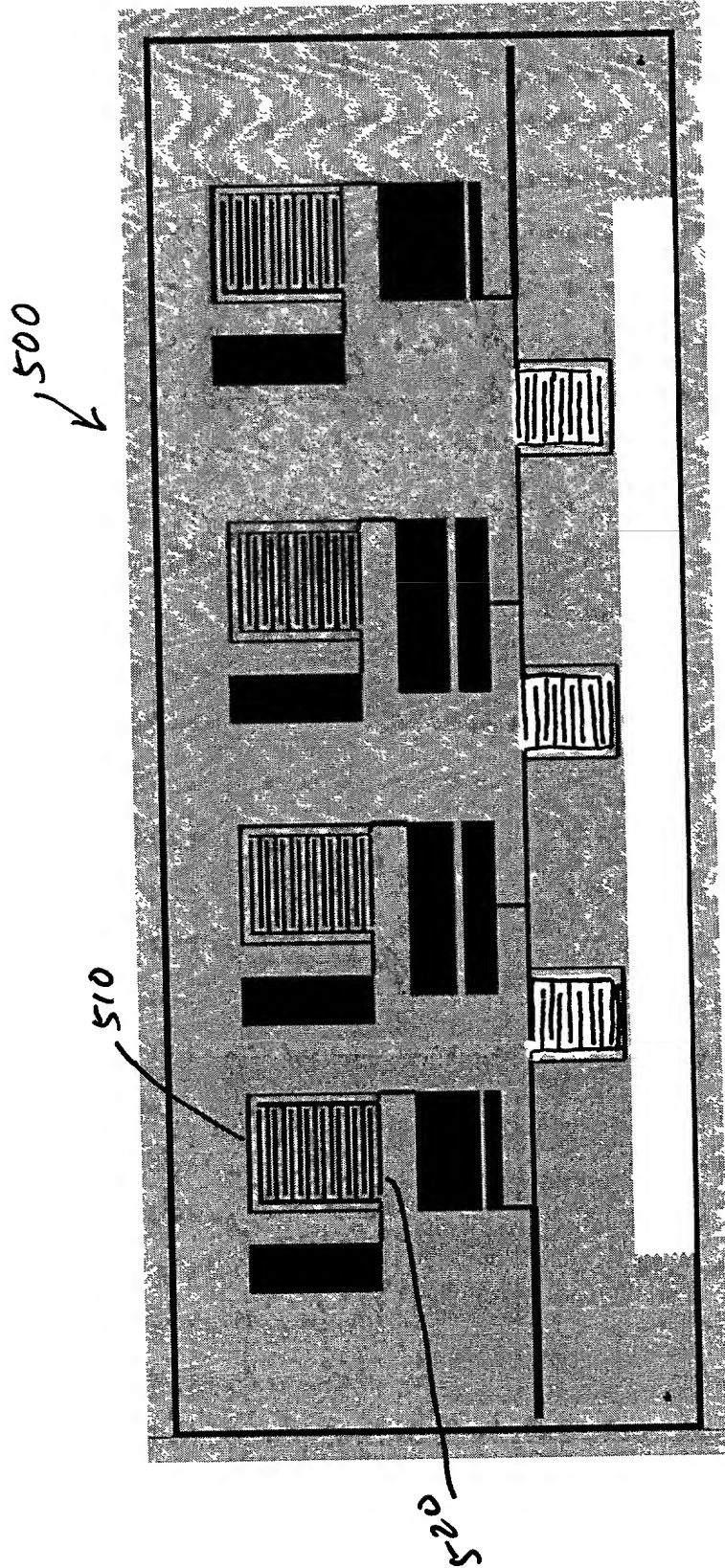


Fig. 5

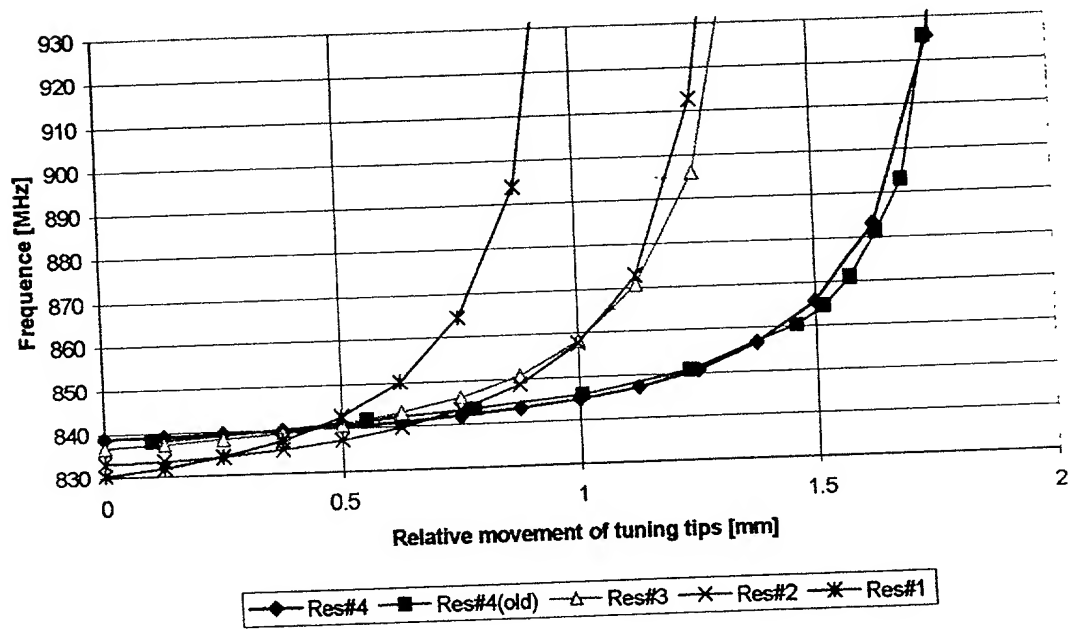


Fig. 6

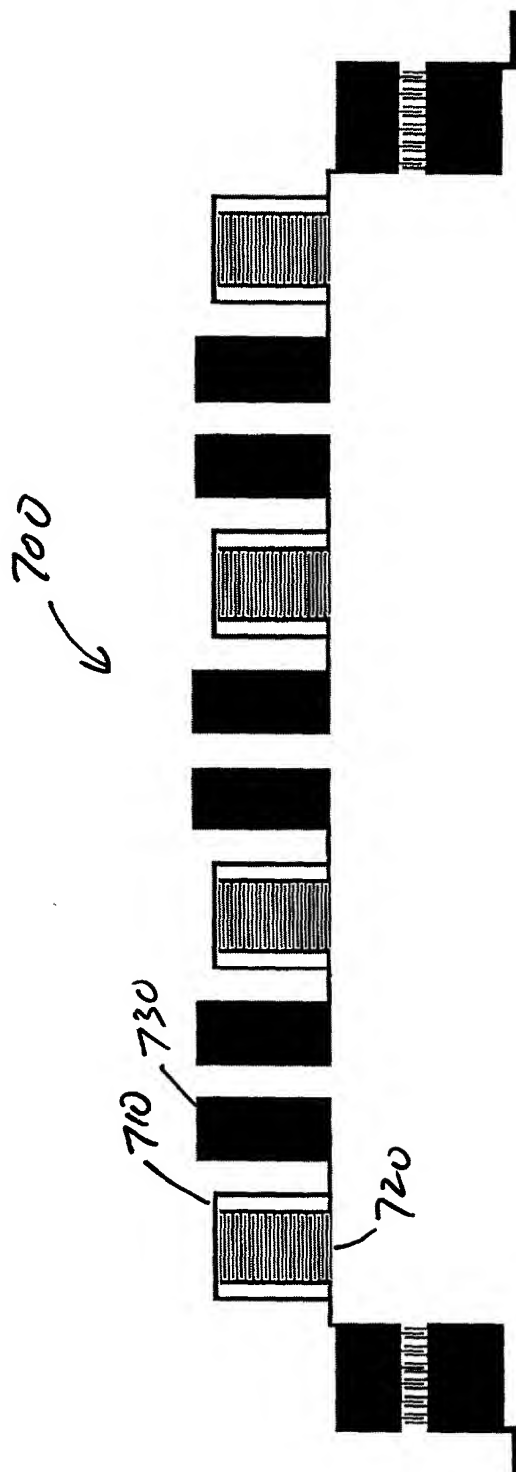


Fig. 7

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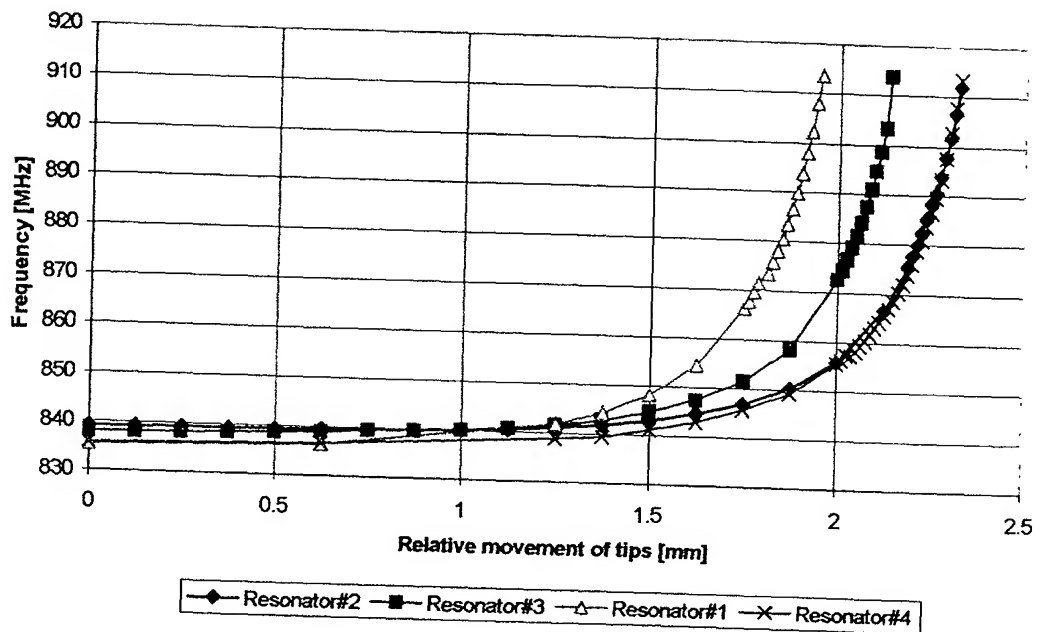


Fig. 8

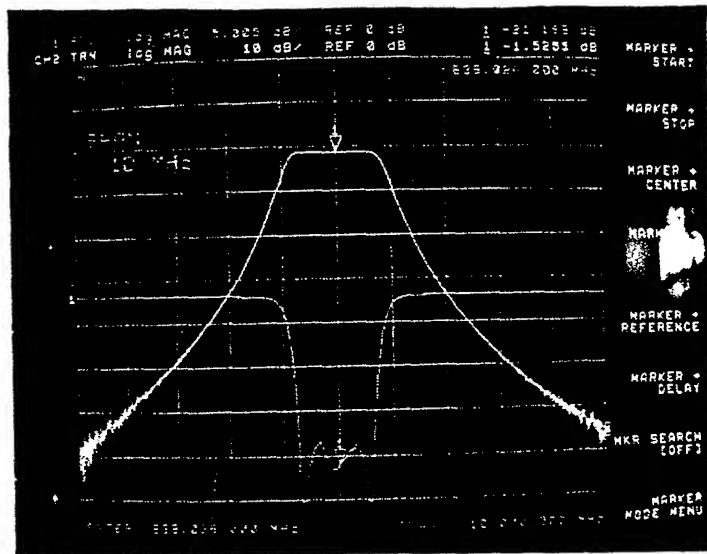


Fig. 9(a)

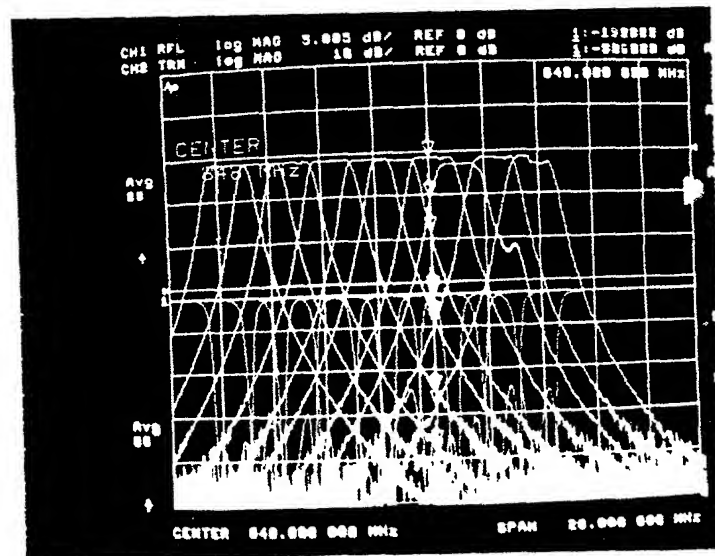


Fig. 9(b)

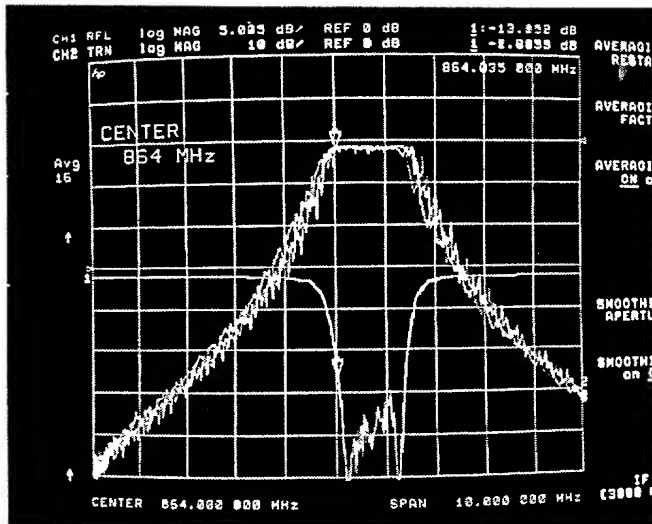


Fig 10(a)

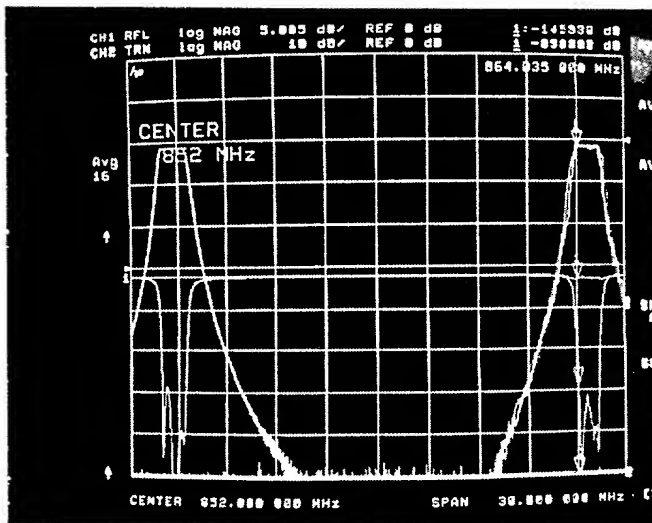


Fig. 10 (b)

10079972622007

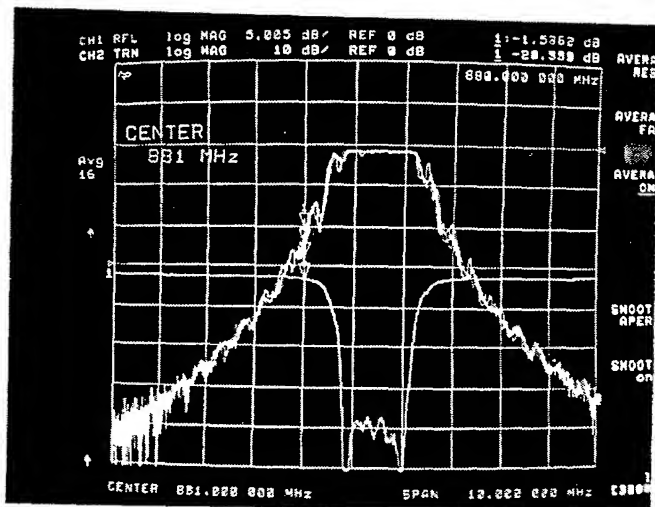


Fig. 11(a)

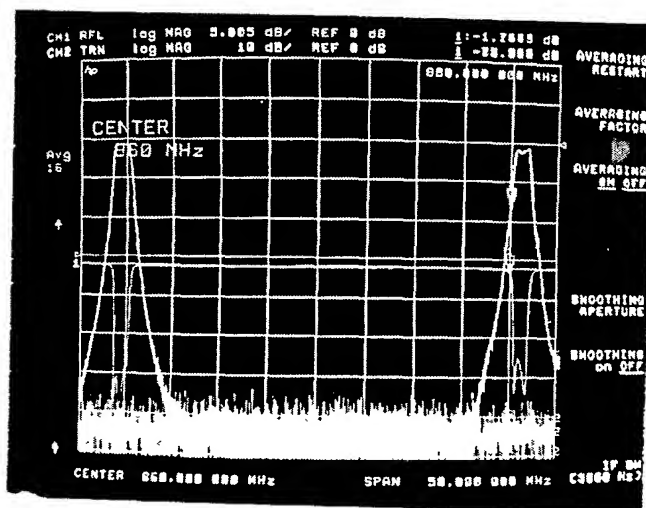


Fig. 11(b)

FOI b7E 2626004

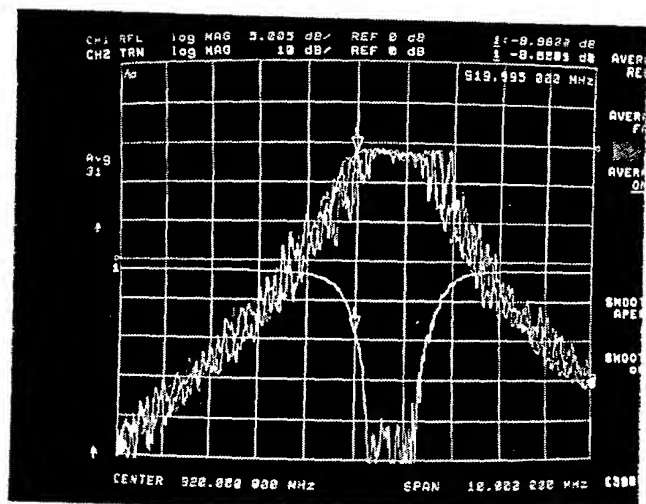


Fig. 12(a)

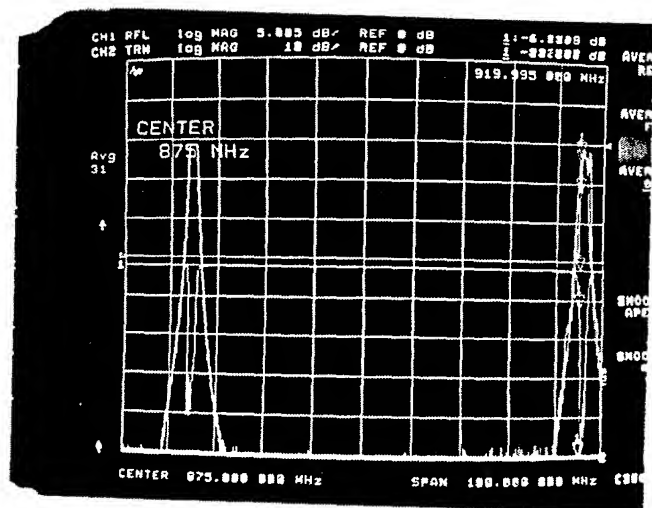


Fig. 12(b)

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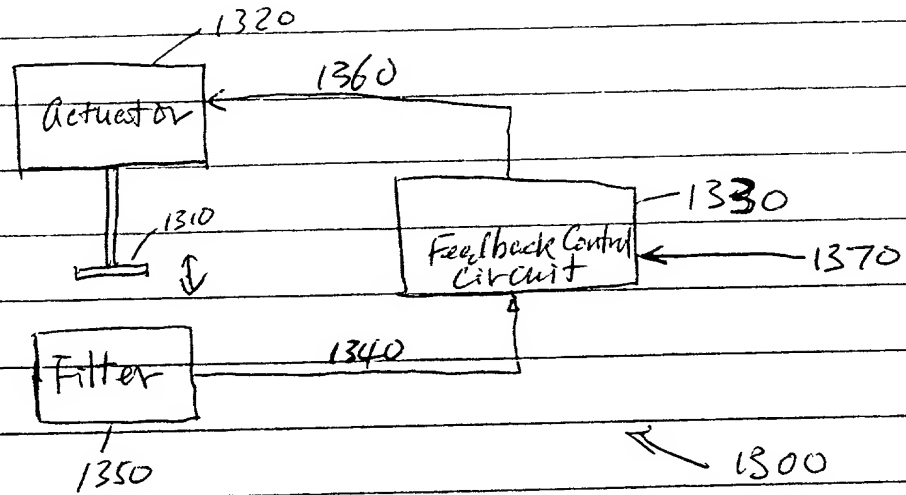


Fig. 13

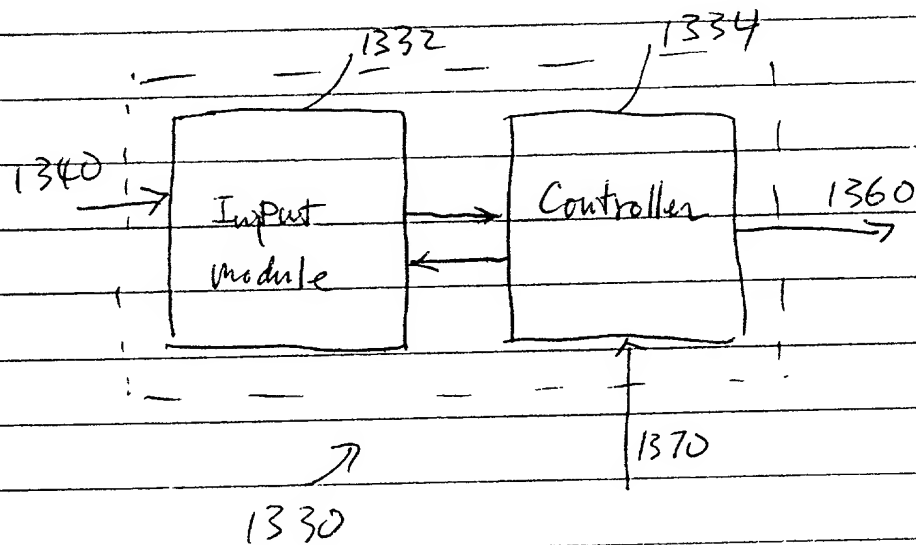
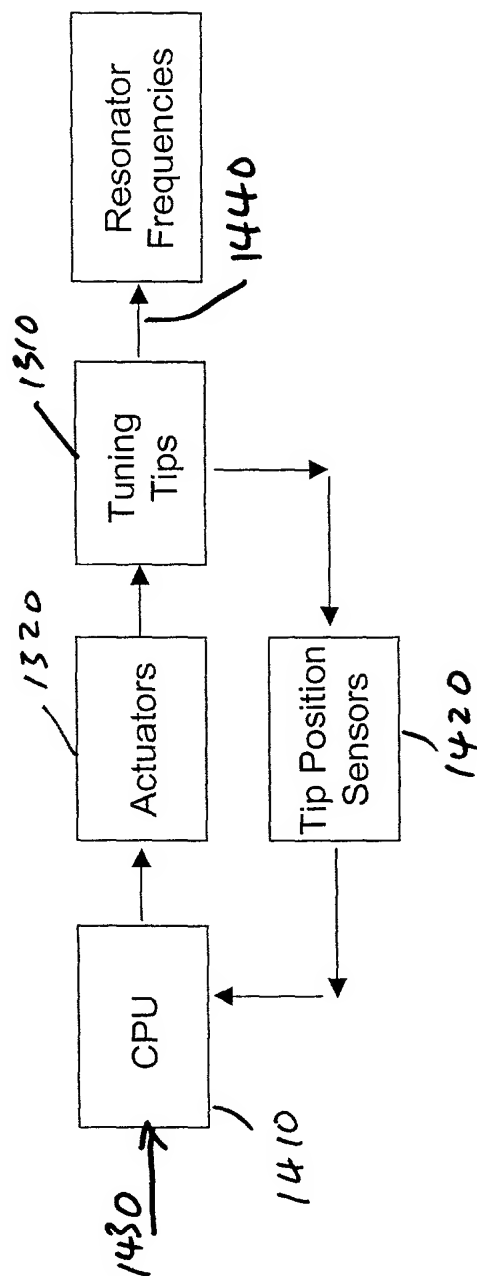


Fig. 14(a)

FIG. 14b

Figure 14b



Control System Block Diagram

1400

Figure 15a

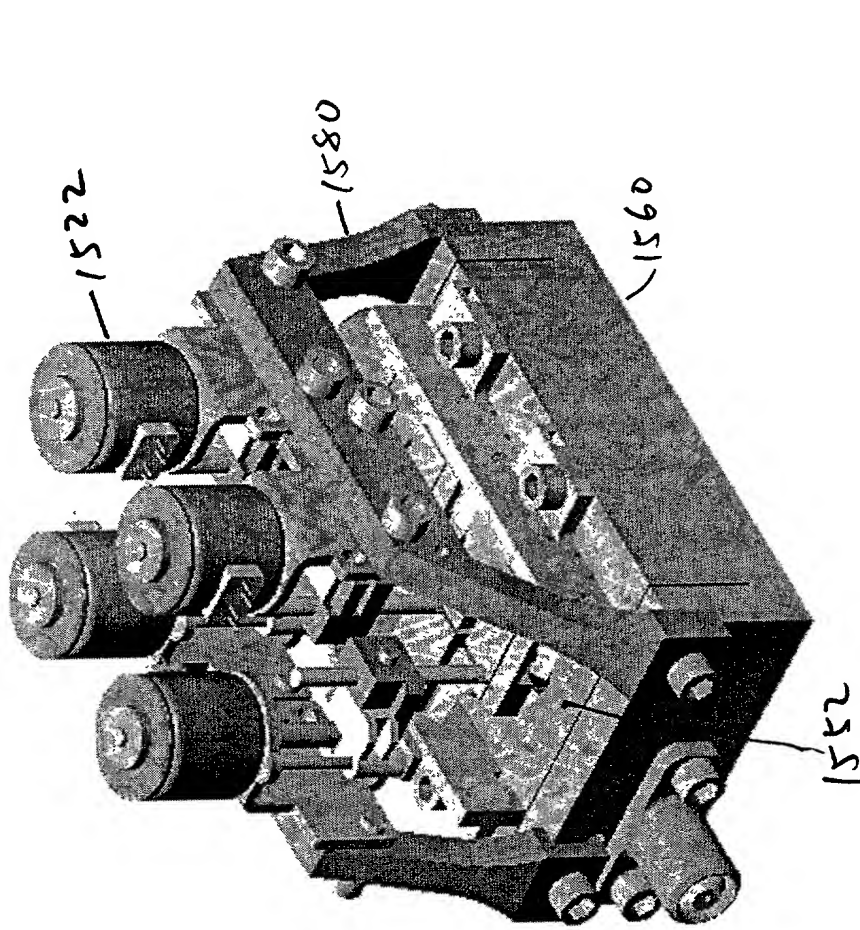
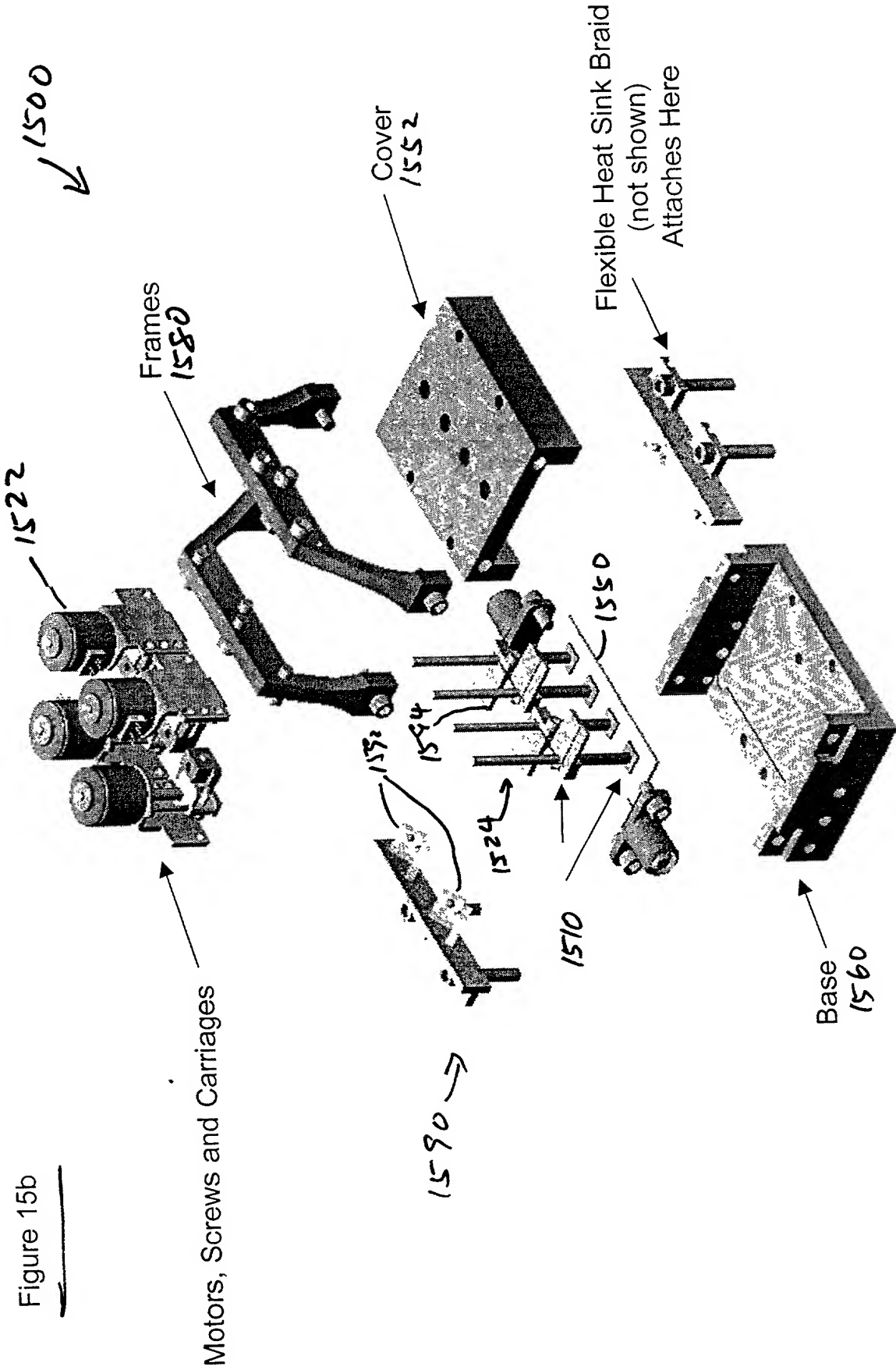


FIGURE 15b



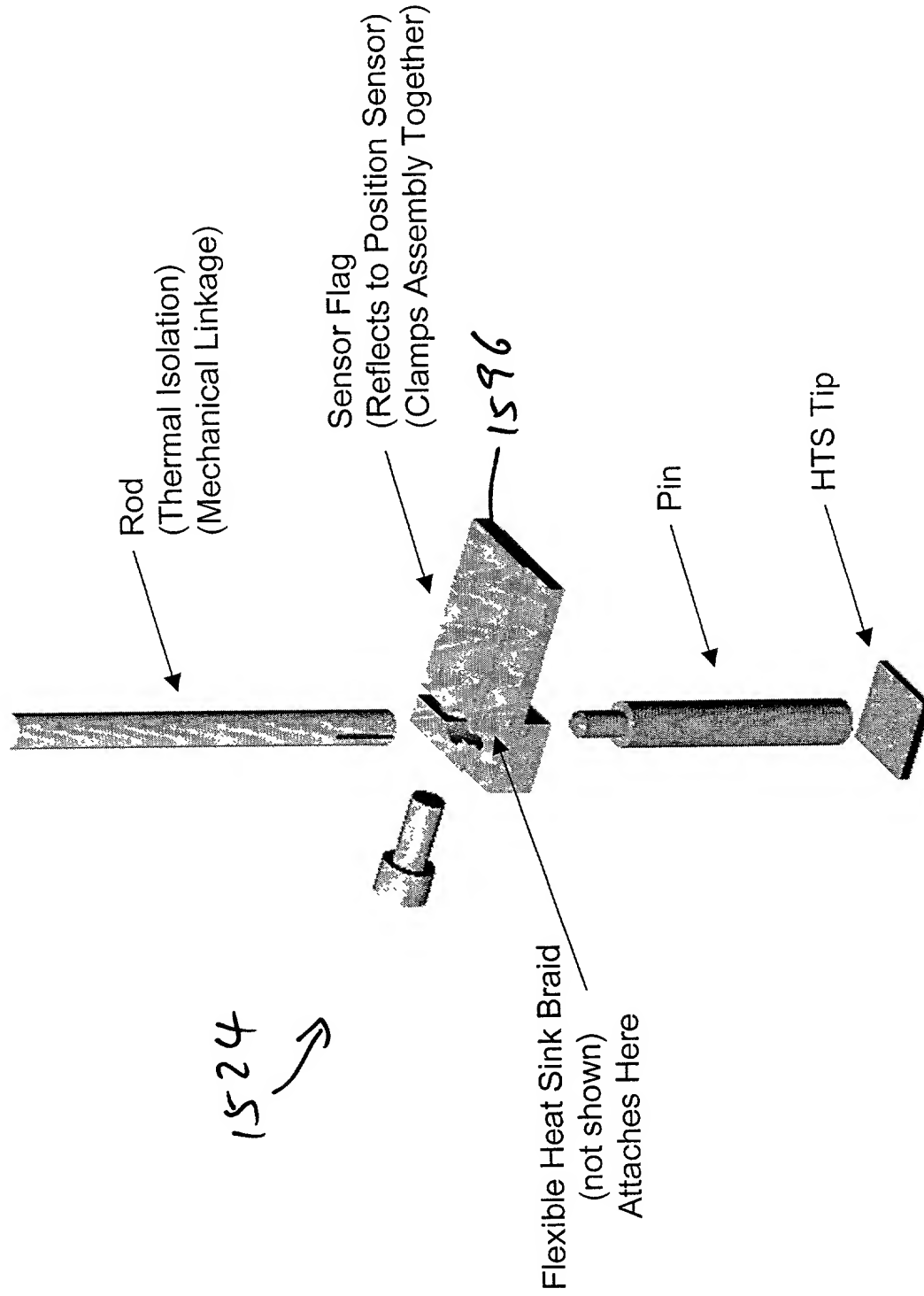


Figure 15c

FIG. 15c

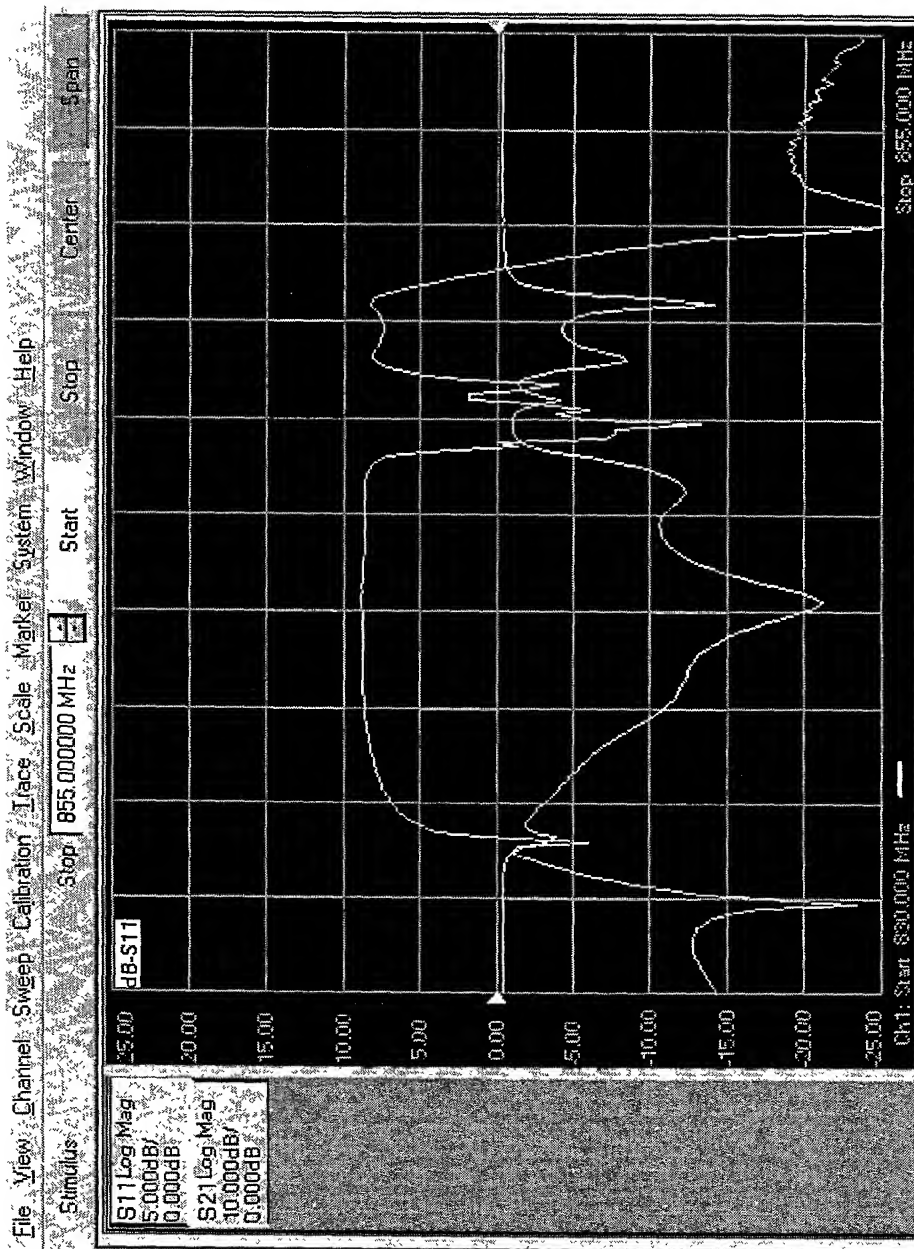


Fig. 16(a)

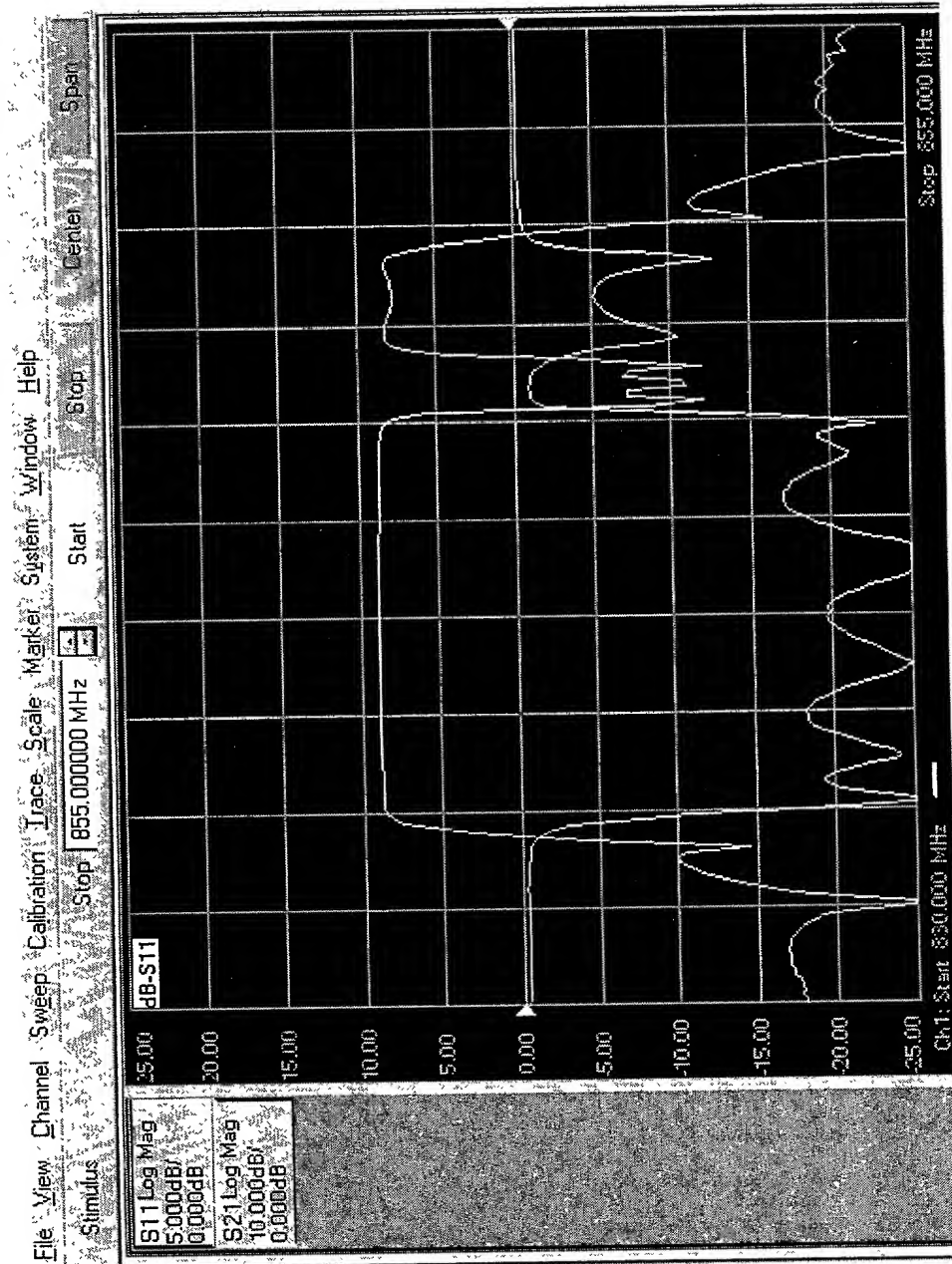


Fig. 16(b)

FIG. 17

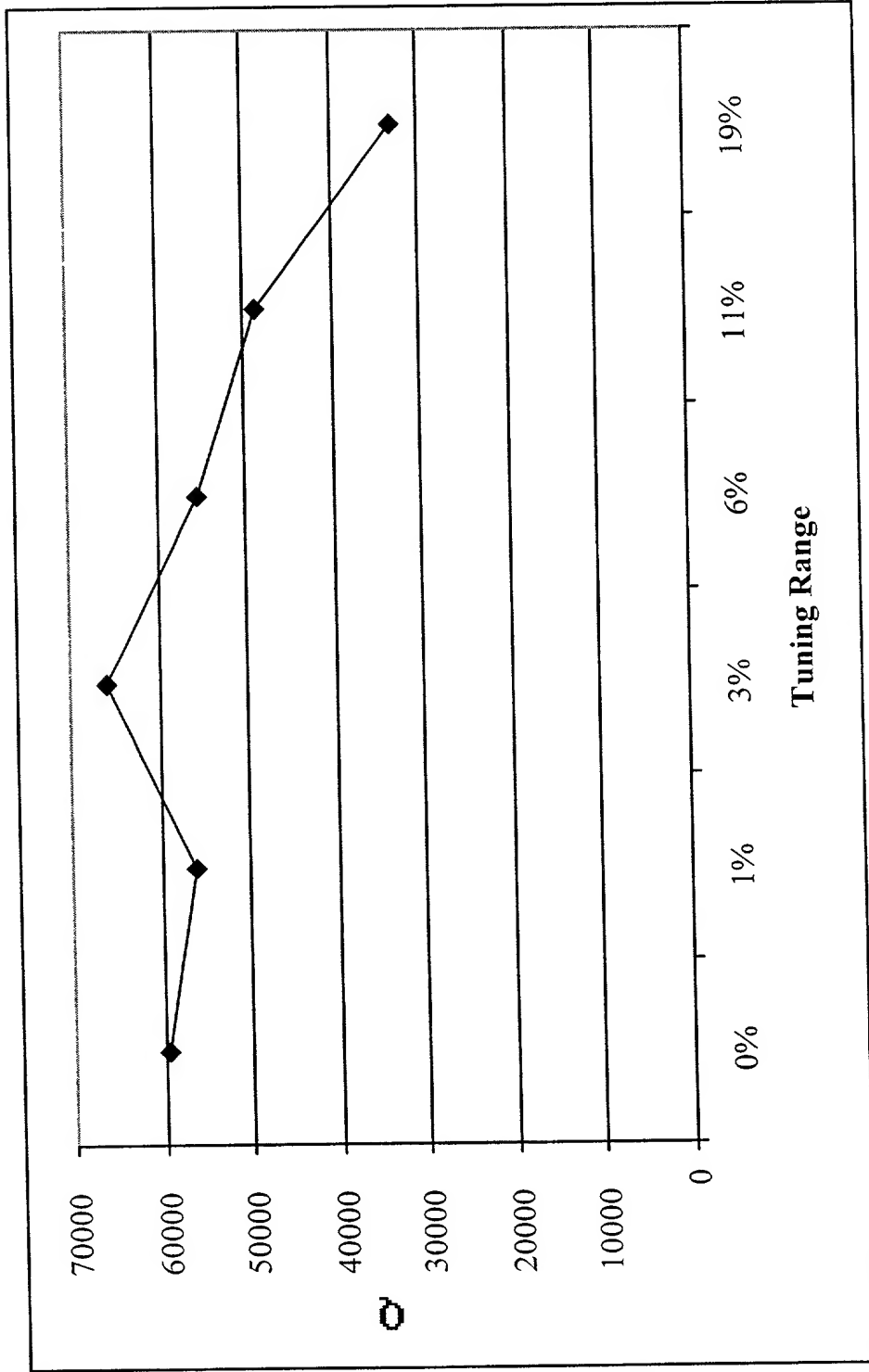


Fig. 17

FIG. 18(a)

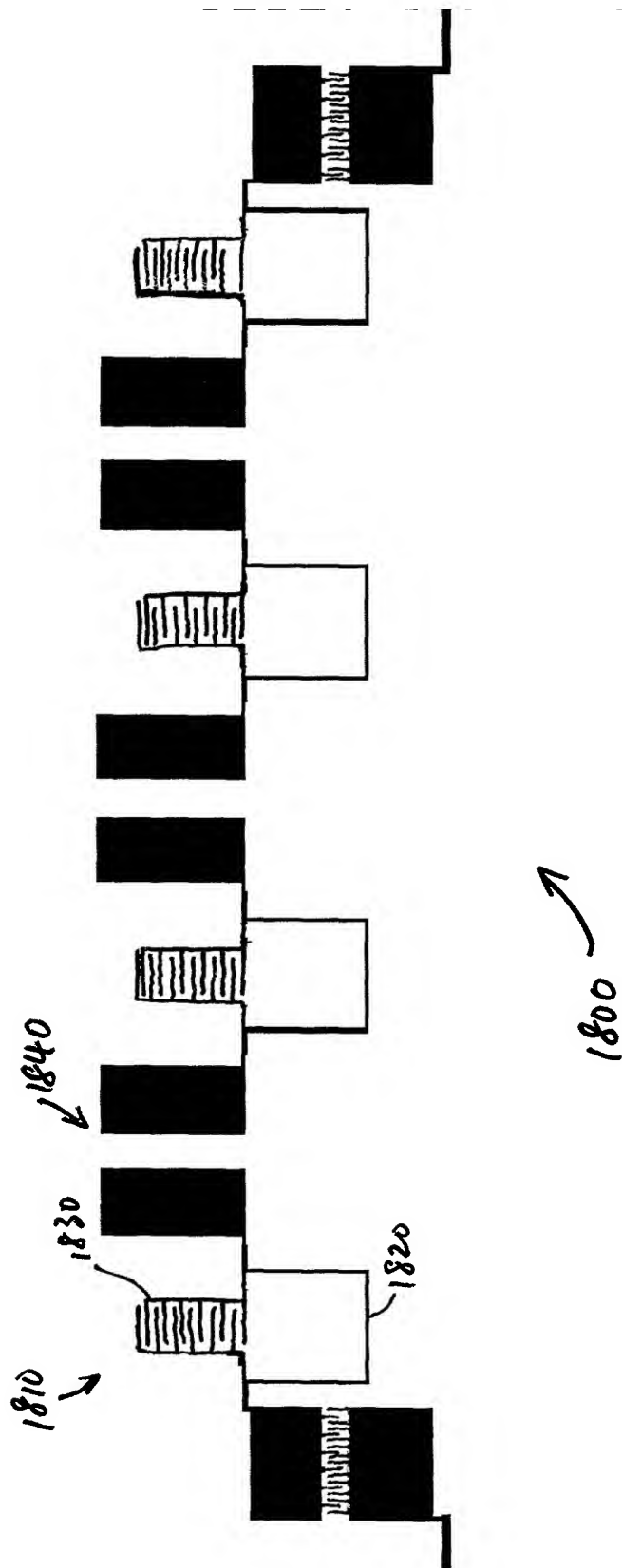


Fig. 18(a)

FIG. 18(b)

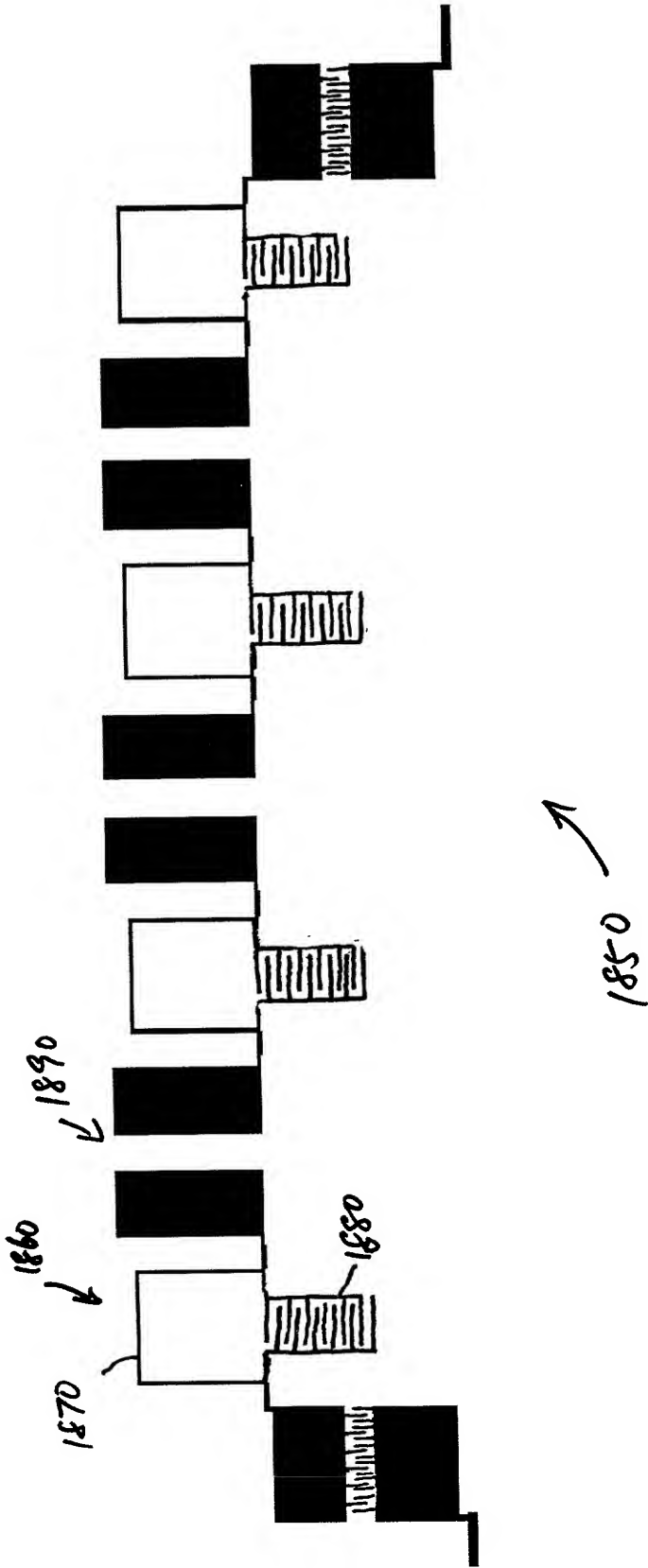


Fig. 18(b)

HTS Tip Tuning of Inductor vs Capacitor

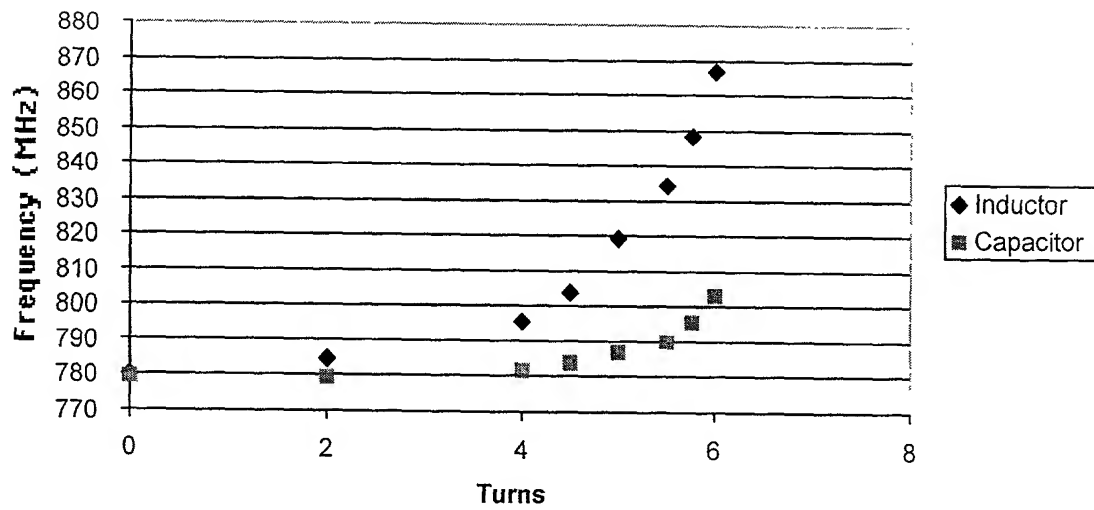


Fig. 19

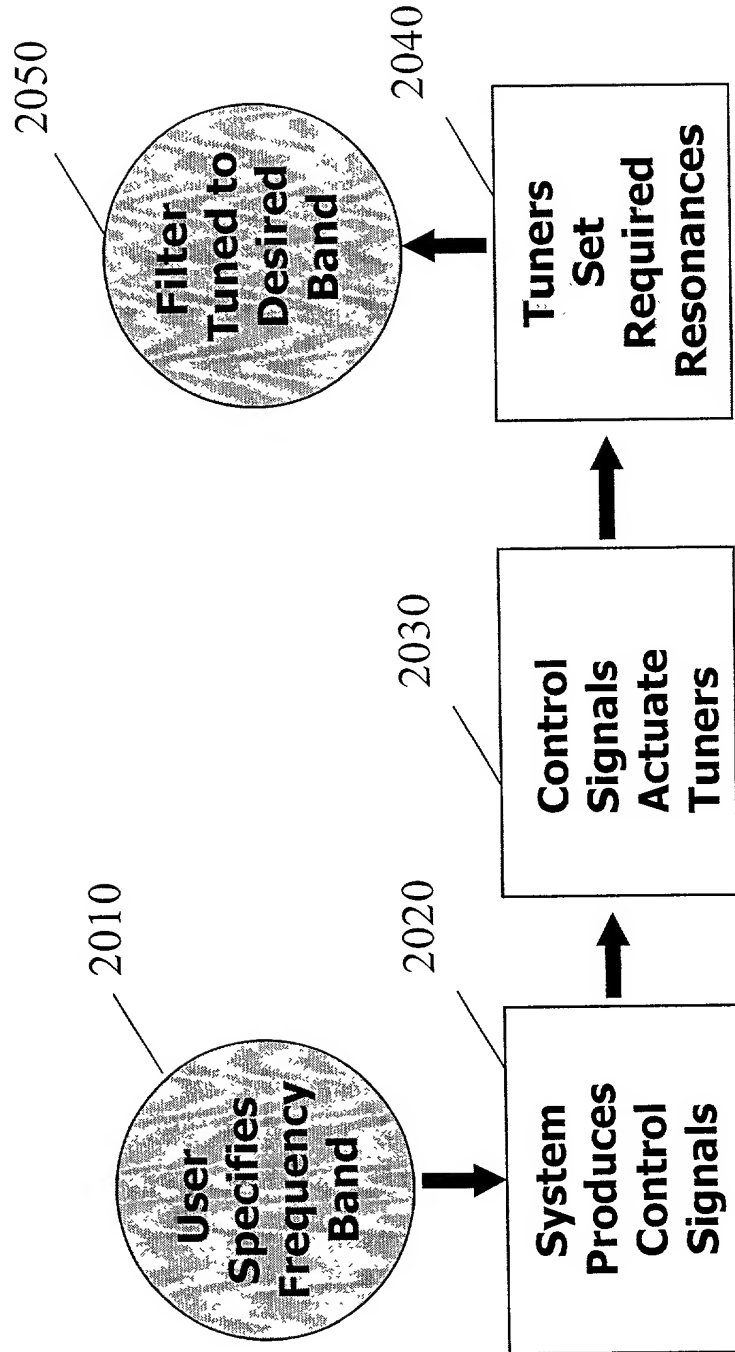


Fig. 20